

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	64	breakpoint and (instruction same (cache or memory)) and (plural\$4 same processor) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag)) and replac\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 10:20
L2	17	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (control near bit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 12:49
L3	13	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (control near bit) and replace\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 12:43
L4	1	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 10:59
L5	1	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 10:59
L6	0	breakpoint and (instruction same (cache or memory)) and (plural\$4 same processor) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag)) with replac\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 11:43
L7	1	"717"/\$.ccls. and breakpoint and (instruction same (cache or memory)) and (plural\$4 same processor) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 11:56
S1	3519	breakpoint and (cache or memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/25 15:19

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S2	997	breakpoint and (cache or memory) and (plural\$4 same processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 09:09
S3	762	breakpoint and (instruction same (cache or memory)) and (plural\$4 same processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 09:10
S4	296	breakpoint and (instruction same (cache or memory)) and (plural\$4 same processor) and (control near bit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 09:10
S5	110	breakpoint and (instruction same (cache or memory)) and (plural\$4 same processor) and (control near bit) and (cache same control same logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 09:11
S6	63	breakpoint and (instruction same (cache or memory)) and (plural\$4 same processor) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/19 09:29
S7	1	"717"/\$.ccls. and (breakpoint and (instruction same (cache or memory)) and (plural\$4 same processor) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/19 09:21
S8	1	"717"/\$.ccls. and breakpoint and (instruction same (cache or memory)) and (plural\$4 same processor) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 11:56
S9	1	"717"/129.ccls. and breakpoint and (instruction same (cache or memory)) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 09:47

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S10	1	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 10:58
S11	3	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (control near bit) and (cache same control same logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 09:49
S12	6	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (cache same control same logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/25 15:19
S13	212	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/19 09:21
S14	11	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (control near bit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/19 09:22
S15	2	"5548717".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 12:34
S16	61	cache same software same breakpoint	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/25 15:19
S17	13	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (control near bit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 10:20

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S19	245	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/25 15:19
S20	64	cache same software same breakpoint	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/25 15:19
S21	3802	breakpoint and (cache or memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/25 15:19
S22	13	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (control near bit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/25 15:19
S23	6	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (cache same control same logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/19 09:20
S24	1	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/19 09:21
S25	1	"717"/\$.ccls. and (breakpoint and (instruction same (cache or memory)) and (plural\$4 same processor) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/19 09:21
S26	131	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and replace\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/19 09:21

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S27	12	"717"/124-129.ccls. and breakpoint and (instruction same (cache or memory)) and (control near bit) and replace\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 10:58
S28	0	breakpoint and (instruction same (cache or memory)) and (plural\$4 same processor) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag)) with replac\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 11:43
S29	63	breakpoint and (instruction same (cache or memory)) and (plural\$4 same processor) and (control near bit) and (cache same control same logic) and (valid\$4 same (indicator or flag)) and replac\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 10:20